

A110-US

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : John H. Coleman
Appl. No. : 09/800,213
Filed : March 6, 2001
For : SEMICONDUCTOR FABRICATION AND STRUCTURE FOR
FIELD-EFFECT AND BIPOLAR TRANSISTOR DEVICES

PETITION AND SUBMISSION OF CORRECTED APPLICATION PAPERS

I hereby certify that this paper is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231

December 11, 2001
Date of Deposit

Peter A. Businger
Attorney Name

27,862
PTO Reg. No.

Peter A. Businger
Signature

Dec 11, 2001
Date of Signature

Hon. Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

In response to the Notice to File Corrected Application Papers dated April 23, 2001, submitted herewith is a substitute drawing set with corrected margins.

This response is being filed immediately on discovery of its due date having been missed, unintentionally, with the application having become abandoned as verified in a call to Telephone No. 1-703- 308-1202. The delay in filing the response is unintentional. and the applicant petitions for revival of the application pursuant to 35 U.S.C. 41(a) (7). Payment of the required fee is enclosed in the Small-Entity amount of \$640.00.

Respectfully submitted,

Peter A. Businger
Peter A. Businger
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